



Dkt. No: SYN-0178

2123
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Manish Jain et al.

Application No.: 10/015,180

Art Unit.: 2123

Filing Date: 11/16/2001

Examiner: Thomas H. Stevens

For: "System And Method For Enhancing The Speed Of Dynamic Timing Simulation Using Delay Assessment At Compile Time"

Mail Stop Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Date: October 7, 2005

AMENDMENT TRANSMITTAL

- Transmitted herewith is an amendment for this application.
- STATUS:** Applicant is other than a small entity.
- EXTENSION OF TERM:** The proceedings herein are for a patent application and the provisions of 37 C.F.R. 1.136 apply. Applicant believes that no extension of term is required. However, this conditional petition is being made to provide for the possibility that applicant has inadvertently overlooked the need for a petition for extension of time.
- FEE FOR CLAIMS:** The fee for claims (37 C.F.R. 1.16(b)-(d)) has been calculated as shown below:

	(Col.1)		(Col. 2)	(Col. 3)	LARGE ENTITY	
	Claims					
	Remaining		Highest No.	Present		Addit.
	After		Previously	Extra	Rate	Fee
	Amendment		Paid For			
Total	44	Minus	44	= 0	x \$50 =	\$0
Indep.	5	Minus	5	= 0	x \$200 =	\$0
First Presentation of Multiple Dependent Claim					+ \$360 =	\$0
Total					Addit. Fee	\$0

No additional fee for claims is required.

- FEE DEFICIENCY:** If any additional extension and/or fee is required, please charge Deposit Account No. 50-0574.

Customer No. 35273

Tel.: (408) 451-5907

SIGNATURE OF PRACTITIONER

Jeanette S. Harms

Reg. No. 35,537

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as FIRST CLASS MAIL in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on October 7, 2005.

Date

10/7/2005

Signature: Rebecca A. Baumann



THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Manish Jain and Badri P Gopalan

Assignee: Synopsys, Inc.

Title: System And Method For Enhancing The Speed Of
Dynamic Timing Simulation Using Delay Assessment
At Compile Time

Serial No.: 10/015,180 File Date: November 16, 2001

Examiner: Thomas H. Stevens Art Unit: 2123

Docket No.: SYN-0178

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AMENDMENT IN RESPONSE TO THE FIRST OFFICE ACTION

Initial Comments

Claims 1-44 are pending in the present application. Claims 23-32 and 39-44 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite. Claims 1-44 are rejected under 35 U.S.C. 103(a) as being obvious by U.S. Patent 5,379,231 (Pillage) in view of U.S. Patent 5,754,826 (Gamal).

Claims 13, 18, 21, 24, and 32 are amended herein to correct typographical and clerical errors.